

<b>FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE</b> <b>(Rev. 2-32) PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	<b>ATTY. DOCKET NO.</b> STE01 P-798B	<b>SERIAL NO.</b> 08/998302
	<b>APPLICANT(S)</b> JOEL D. STANFIELD ET AL.	
	<b>FILING DATE</b>	<b>GROUP</b>

JD542 U.S. PTO  
08/998302  
12/24/97

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPRO- PRIATE
BE	3 7 0 1 9 8 7	10/31/72	Leighton			
	3 7 5 7 9 4 4	09/11/73	Goodman			
	3 9 1 3 0 8 7	10/14/75	McBrian			
	4 2 1 9 2 9 6	08/26/80	Fuji et al.			
	4 3 7 6 9 3 6	03/15/83	Kott			
	4 4 5 7 0 1 6	06/26/84	Pfeffer			
	4 4 7 6 4 6 9	10/09/84	Lander			
BT	4 5 9 5 9 2 2	06/17/86	Cobb et al.			

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLA- TION
						Y N
BE	0 3 6 1 9 8 9	04/04/90	EPC			
BE	4- 2 6 8 9 9 4	09/24/92	Japan			
BE	2- 1 8 5 4 9 7	07/19/90	Japan			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
BE	"The New Era of the Lan," Distribute Data Control Access, Dallas Semiconductor.
BE	"Automatic Identification Data Book," Dallas Semiconductor, pages 194-205, 1994.

EXAMINER <i>Brian Zimmerman</i>	DATE CONSIDERED <i>8/25/98</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPRO- PRIATE
BZ	4 6 3 6 6 3 4	01/13/87	Harper et al.			
	4 6 3 6 9 5 0	01/13/87	Caswell et al.			
	4 6 8 8 0 2 6	08/18/87	Scribner et al.			
	4 8 1 4 7 4 2	03/21/89	Morita et al.			
	4 8 1 8 8 5 5	04/04/89	Mongeon et al.			
	4 8 6 2 1 6 0	08/29/89	Ekchian et al.			
	4 8 6 6 2 5 5	09/12/89	Sing			
BE	4 8 7 9 7 5 6	11/07/89	Stevens et al.			

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLA- TION
						Y N
BE	56- 1 1 0 1 9 3	09/01/81	Japan			

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

EXAMINER INITIAL	
BZ	"Touch the Future," Automatic Identification by Dallas Semiconductor.
BE	"Advanced Information," DS2405 Addressable Switch, Dallas Semiconductor.

EXAMINER <i>Brian Zimmerman</i>	DATE CONSIDERED <i>8/25/96</i>
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BZ	4 9 2 4 2 1 9	05/08/90	Satō			
	4 9 7 3 0 8 6	11/27/90	Donnelly et al.			
	5 0 6 3 3 8 0	11/05/91	Wakura			
	5 1 1 3 3 4 9	05/12/92	Nakamura et al.			
	5 2 1 6 6 1 8	06/01/93	Arita et al.			
	5 2 1 8 4 6 6	06/08/93	Brooks			
	5 2 8 7 4 1 4	02/15/94	Foster			
BZ	5 3 1 7 3 0 9	05/31/94	Vercellotti et al.			

FOREIGN PATENT DOCUMENTS

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						Y N

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
BZ	"User's Manual," DS9092K Touch Memory Starter Kit, Version 3.0, Dallas Semiconductor, February 1993.
BZ	"50 Ways to Touch Memory," Third Edition, Dallas Semiconductor, August 1994.

EXAMINER <i>BZimmerman</i>	DATE CONSIDERED <i>8/25/98</i>
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62	5 3 2 7 1 1 5	07/05/94	Swierczek			
	5 3 3 4 8 2 2	08/02/94	Sanford			
	5 3 8 9 9 1 9	02/14/95	Warren et al.			
	5 4 2 4 8 5 8	06/13/95	Gillotte			
	5 4 2 6 2 8 4	06/20/95	Doyle			
	5 4 5 0 0 7 0	09/12/95	Massar et al.			
	5 4 5 5 4 0 9	10/03/95	Smith et al.			
	5 4 5 5 4 1 0	10/03/95	Schneider			
62	5 4 6 1 3 9 0	10/24/95	Hoshen			

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						Y N

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
62	"Book of DS19xx Touch Memory Standards," Dallas Semiconductor, February 1994.
62	"DS19x Touch Memory Used in Paris Garbage Collection."

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